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DESCRIPTION

PLASMA DISPLAY PANEL

5 This application is a U.S. National Phase application of PCT
International Application PCT/JP2005/008648

TECHNICAL FIELD

10 The present invention relates to a plasma display panel for use in a
display device or the like.

BACKGROUND ART

 The plasma display panel (hereafter referred to as PDP) consists
basically of a front substrate and a rear substrate.

15 The front substrate comprises: a glass substrate; display electrodes
including stripe-like transparent electrodes and bus electrodes formed on a
principal surface of the glass substrate; a dielectric layer covering the
display electrodes to act as a capacitor; and a protective layer composed of
MgO formed on the dielectric layer.

20 The glass substrate adopts a glass substrate produced by float process,
a glass manufacturing technology easy for large-sizing and excellent in
flattening. The display electrodes include transparent electrodes provided
by the TFT (thin film transistor) processing, on which predetermined
patterns are formed using a paste including Ag to obtain a sufficient
25 electrical conductivity before sintering it to form the bus electrodes. The
dielectric glass layer is formed by sintering a dielectric paste coated so as to
cover the display electrodes having the transparent electrodes and bus